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web based circuit design
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1   IP delivery for FPGAs using Applets and JHDL
◈   Michael J. Wirthlin, Brian McMurtrey
    June 2002   DAC '02: Proceedings of the 39th conference on Design automation
    Publisher: ACM
    Full text available: 🗎 pdf(198.07 KB)   Additional Information: full citation, abstract, references, index terms

    Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 23, Citation Count: 1

        This paper introduces an FPGA IP evaluation and delivery system that operates within Java appl
        use of such applets allows designers to create, evaluate, test, and obtain FPGA circuits directly v
        web browser. Based on the JHDL design tool, ...

        Keywords: FPGA, JHDL, applet, intellectual property

2   Proceedings of the 2004 international symposium on Low power electronics and design
◈   Rajiv Joshi, Kiyoung Choi, Vivek Tiwari, Kaushik Roy
    August 2004 proceeding
    Publisher: ACM
    Additional Information: full citation, abstract

    Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

        Welcome to the 2004 International Symposium on Low Power Electronics and Design (ISLPED)!
        year ISLPED moves back to California after a very successful year in Asia. ISLPED started as a v
        on low-power design in 1994 and is in ...

3   Using verilog HDL to teach computer architecture concepts
◈   Daniel C. Hyde
    June 1998   WCAE '98: Proceedings of the 1998 workshop on Computer architecture education
    Publisher: ACM
    Full text available: 🗎 pdf(60.74 KB)   Additional Information: full citation, abstract

    Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 87, Citation Count: 1

        Students in computer architecture courses, especially undergraduates, need to design computer
        components in order to gain an in-depth understanding of architectural concepts. For maximum
        students must be active learners, engage the material ...

4 From a Hyperdocument-Centric to an Object-Oriented Approach for the Cave Project
   L. Soares Indrusiak, R. Augusto da Luz Reis
   September 2000 SBCCI '00: Proceedings of the 13th symposium on Integrated circuits and systems
   Publisher: IEEE Computer Society
   Full text available: 📄 Publisher Site   Additional Information: full citation, abstract

   Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

      This paper briefly describes the new results of the Cave Project. Cave Project is a research initia
      aiming to make possible a user-transparent distribution of CAD resources over the World Wide W
      Early results on developing a web based interface ...

      Keywords: CAD resources, Cave Project, Internet, World Wide Web, circuit CAD, design flow m
      integrated circuit design, integrated circuits design, object-oriented databases, object-oriented c
      object-oriented methods, research initiative, research initiatives, user-transparent distribution, v
      based interface

5 Web engineering with the visual software circuit board
   Hovhannes Avoyan, Barry Levine
   May 2004 WWW Alt. '04: Proceedings of the 13th international World Wide Web conference on A
              track papers & posters
   Publisher: ACM
   Full text available: 📄 pdf(157.89 KB)   Additional Information: full citation, abstract, references, index terms, revie

   Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 21, Citation Count: 0

      The Visual Software Circuit Board (VSCB) platform supports a component based development
      methodology towards the development of software systems. The circuit board design techniques
      methodologies have evolved for electronic device and component engineering ...

      Keywords: circuit based software development, component based development, rapid applicati
      development, visual programming, web application development, web engineering

6 ACM SIGCOMM computer communication review
   Andrew T. Campbell, Mischa Schwartz
   October 2001 ACM SIGCOMM Computer Communication Review, Volume 31 Issue 5
   Publisher: ACM
   Full text available: 📄 pdf(376.25 KB)   Additional Information: full citation, abstract

   Bibliometrics: Downloads (6 Weeks): 25, Downloads (12 Months): 51, Citation Count: 0

      At some point in the future, how far out we do not exactly know, wireless access to the Internet
      outstrip all other forms of access bringing the freedom of mobility to the way we access the web
      communicate with each other, and conduct business. ...

7 NBTI resilient circuits using adaptive body biasing
   Zhenyu Qi, Mircea R. Stan
   May 2008 GLSVLSI '08: Proceedings of the 18th ACM Great Lakes symposium on VLSI
   Publisher: ACM
   Full text available: 📄 pdf(273.84 KB)   Additional Information: full citation, abstract, references, index terms

   Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 0, Citation Count: 0

      Reliability has become a practical concern in today's VLSI design with advanced technologies. In

sensors have been proposed for reliability monitoring to provide advance warnings before systei occur. This paper presents a reliability monitor ...

Keywords: body bias, nbti, reliability

8 An evolutionary platform for developing next-generation electronic circuits
James A. Hilder, Andy M. Tyrrell
July 2007   GECCO '07: Proceedings of the 2007 GECCO conference companion on Genetic and evolutionary computation
Publisher: ACM
Full text available: pdf(631.53 KB)   Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 102, Citation Count: 0

In this paper, a new method for evolving simple electronic circuits is discussed, with the aim of improving the reliability and performance of basic circuit blocks. Next-generation CMOS device r will be used in the simulation of circuits. Circuits ...

Keywords: CMOS, SPICE, analogue circuit design, genetic algorithms, genetic programming

9 Proceedings of the 2005 conference on Genetic and evolutionary computation
Una-May O'Reilly, Hans-Georg Beyer
June 2005   proceeding
Publisher: ACM
Additional Information: full citation, appendices and supplements, abstract

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 1

The papers in this two volume proceedings are presented at the 7th Annual Genetic and Evolutic Computation COnference (GECCO-2005), held in Washington, D.C., June 25-29, 2005.This year exceptional one for the GECCO conference series. First, ...

10 Proceedings of the 18th ACM Great Lakes symposium on VLSI
Vijay Narayanan, Zhiyuan Yan, Enrico Macii, Sanjukta Bhanja
May 2008   proceeding
Publisher: ACM
Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

Welcome to the 18th edition of the *Great Lakes Symposium on VLSI (GLSVLSI) 2008* held in Orl Florida, the premium tourist attraction of the sunshine state. Since its first meeting in March 199 Kalamazoo, Michigan, GLSVLSI has traveled ...

11 Proceedings of the 9th annual conference on Genetic and evolutionary computation
Hod Lipson
July 2007   proceeding
Publisher: ACM
Additional Information: full citation, appendices and supplements, abstract

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

These proceedings contain the papers presented at the 9th Annual Genetic and Evolutionary Con

COnference (GECCO-2007), held in London, UK, July 7-11, 2007. For the first time GECCO was outside the US. This clearly proved ...

12 Proceedings of the 2008 ACM symposium on Applied computing
Roger L. Wainwright, Hisham M. Haddad
March 2008 proceeding
Publisher: ACM
Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

Welcome to the 23rd Annual ACM Symposium on Applied Computing (SAC 2008). This internatic event is dedicated to computer scientists, engineers, and practitioners seeking innovative ideas various areas of computer applications. This year, the conference ...

13 ACM SIGCSE Bulletin: Volume 40 Issue 1
February 2008 issue   Volume 40 Issue 1
Publisher: ACM
Additional Information: full citation

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

14 Proceedings of the 39th SIGCSE technical symposium on Computer science education
J. D. Dougherty, Susan Rodger, Sue Fitzgerald, Mark Guzdial
March 2008 proceeding
Publisher: ACM
Additional Information: full citation

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

15 Proceedings of the 2007 GECCO conference companion on Genetic and evolutionary com
Peter A. N. Bosman, Tina Yu, Anikó Ekárt
July 2007   proceeding
Publisher: ACM
Additional Information: full citation, appendices and supplements

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

16 The state of the art in locally distributed Web-server systems
Valeria Cardellini, Emiliano Casalicchio, Michele Colajanni, Philip S. Yu
June 2002   ACM Computing Surveys (CSUR)   Volume 34 Issue 2
Publisher: ACM
Full text available: pdf(1.41 MB)   Additional Information: full citation, abstract, references, cited by, index ter

Bibliometrics: Downloads (6 Weeks): 82, Downloads (12 Months): 682, Citation Count: 22

The overall increase in traffic on the World Wide Web is augmenting user-perceived response tir

popular Web sites, especially in conjunction with special events. System platforms that do not re
information content cannot provide the needed ...

Keywords: Client/server, World Wide Web, cluster-based architectures, dispatching algorithms
distributed systems, load balancing, routing mechanisms

17 Computing curricula 2001
CORPORATE The Joint Task Force on Computing Curricula
September 2001 Journal on Educational Resources in Computing (JERIC), Volume 1 Issue 3es
Publisher: ACM
Full text available: 🔲 pdf(613.63 KB) 📄 html(2.78 KB) Additional Information: full citation, references, cited by, index te

Bibliometrics: Downloads (6 Weeks): 159, Downloads (12 Months): 1717, Citation Count: 22

18 System-level power optimization: techniques and tools
Luca Benini, Giovanni de Micheli
April 2000   ACM Transactions on Design Automation of Electronic Systems (TODAES), Volu
                2
Publisher: ACM
Full text available: 🔲 pdf(385.22 KB)   Additional Information: full citation, abstract, references, cited by, index ter

Bibliometrics: Downloads (6 Weeks): 45, Downloads (12 Months): 537, Citation Count: 68
This tutorial surveys design methods for energy-efficient system-level design. We consider elect
sytems consisting of a hardware platform and software layers. We consider the three major con
of hardware that consume energy, namely computation, ...

19 Image Retrieval from the World Wide Web: Issues, Techniques, and Systems
M. L. Kherfi, D. Ziou, A. Bernardi
March 2004 ACM Computing Surveys (CSUR), Volume 36 Issue 1
Publisher: ACM
Full text available: 🔲 pdf(294.13 KB)   Additional Information: full citation, abstract, references, cited by, index ter

Bibliometrics: Downloads (6 Weeks): 123, Downloads (12 Months): 1122, Citation Count: 6
With the explosive growth of the World Wide Web, the public is gaining access to massive amou
information. However, locating needed and relevant information remains a difficult task, whethe
information is textual or visual. Text search engines ...

Keywords: Image-retrieval, World Wide Web, crawling, feature extraction and selection, indexi
relevance feedback, search, similarity

20 An enhanced flow model for constraint handling in hierarchical multi-view design environme
Pieter van der Wolf, Olav ten Bosch, Alfred van der Hoeven
November 1994 ICCAD '94: Proceedings of the 1994 IEEE/ACM international conference on Comput
                design
Publisher: IEEE Computer Society Press
Full text available: 🔲 pdf(958.27 KB)   Additional Information: full citation, abstract, references, cited by, index ter

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 7, Citation Count: 2
In this paper we present an enhanced design flow model that increases the capabilities of a CAD
framework to support design activities on hierarchical multi-view design descriptions. This flow r

offers new constructs for the configuration of complex ...

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